

RESPONSE UNDER 37 C.F.R. § 1.116

EXPEDITING PROCEDURE

EXAMINING GROUP 2800

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To Examiner: Asok K. Sarkar	Total Pages Sent: 16
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Facsimile Number: (571) 273-8300	Transmission Date: February 28, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Low, <i>et al.</i>	Docket No.:	02 P 15173 US / INTECH 3.0-076
Serial No:	10/689,233	Art Unit:	2891
Date Filed:	October 20, 2003		
TITLE:	Inclusion Of Low-K Dielectric Material Between Bit Lines		

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Response Under 37 C.F.R. § 1.116 (15 pages)

Respectfully submitted,



Kristy Engeldahl
Legal Assistant

Confirmation Respectfully Requested

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Applicant: Low, *et al.* Docket No.: 02 P 15173 US / INTECH
3.0-076
Serial No.: 10/689,233 Art Unit: 2891
Filed: October 20, 2003 Examiner: Asok K. Sarkar
For: Inclusion Of Low-K Dielectric Material Between Bit Lines

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.116

Dear Sir:

The following remarks are presented in response to the Examiner's Final Office Action
mailed December 28, 2005.